

Abstract

The present invention is applied to, for example, a liquid crystal display device having a driving circuit integrally formed on an insulating substrate, and makes it possible to effectively avoid a variation in delay time in a logical circuit using TFTs or the like by inserting dummy data (DD) into input data (D1) and forcedly switching the logical level of the input data (D1) at a predetermined timing during a quiescent period (T2) in which the input data is held at a constant logical level.